10 WATT WD DUAL “LOW INPUT” SERIES DC/DC CONVERTERS

Features

- Universal 9 to 36 Volt Input Range
- Up to 10 Watts of PCB Mounted Power
- Efficiencies to > 80%
- Optional On/Off Control Pin
- Fully isolated, Filtered Design
- Low Noise Outputs
- Very Low I/O Capacitance, 350 pF Typical
- Water Washable Shielded Copper Case
- Five Year Warranty

Description

The universal input of the WD Dual series spans 9 to 36 volts. This makes these converters ideal for 12, 24 and 28 volt battery and process control applications.

Coupled with these features is a low output noise of typically less than 80 mV peak to peak. The noise is also fully specified for RMS value and if even these impressive noise figures aren’t enough, our applications section shows a simple add on circuit that can reduce the output noise to less than 10 mV p-p.

Full isolation is provided to help cut ground loops in industrial systems, where unknown input power quality could create havoc with sensitive, high precision analog circuitry.

Selection Chart

<table>
<thead>
<tr>
<th>Model</th>
<th>Input Range VDC</th>
<th>Vout VDC</th>
<th>Iout ADC</th>
<th>Power W</th>
</tr>
</thead>
<tbody>
<tr>
<td>24D5.850WD</td>
<td>9</td>
<td>36</td>
<td>±5</td>
<td>±850</td>
</tr>
<tr>
<td>24D12.400WD</td>
<td>9</td>
<td>36</td>
<td>±12</td>
<td>±400</td>
</tr>
<tr>
<td>24D15.320WD</td>
<td>9</td>
<td>36</td>
<td>±15</td>
<td>±320</td>
</tr>
</tbody>
</table>

No extra components or heatsinking are required for most applications saving you design time and valuable PCB space.

What all this means to you is a tighter, more compact overall system that has the capability of being universally powered. Full application information is provided to make integrating this supply in your system a snap.

Other input and output voltage combinations may be factory ordered, contact CALEX applications engineering at 1-800-542-3355 for more information.
### Input Parameters*

<table>
<thead>
<tr>
<th>Model</th>
<th>24D5.850WD</th>
<th>24D12.400WD</th>
<th>24D15.320WD</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage Range</td>
<td>MIN</td>
<td>9</td>
<td>36</td>
<td>VDC</td>
</tr>
<tr>
<td></td>
<td>MAX</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Current</td>
<td>Full Load</td>
<td>450</td>
<td>510</td>
<td>510</td>
</tr>
<tr>
<td></td>
<td>No Load</td>
<td>10</td>
<td>15</td>
<td>15</td>
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<tr>
<td>Efficiency</td>
<td>TYP</td>
<td>79</td>
<td>78</td>
<td>78</td>
</tr>
<tr>
<td>Switching Frequency</td>
<td>TYP</td>
<td>100</td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>Maximum Input Overvoltage</td>
<td>MAX</td>
<td>40</td>
<td></td>
<td>VDC</td>
</tr>
<tr>
<td>Turn-on Time, 1% Output Error</td>
<td>TYP</td>
<td>10</td>
<td>20</td>
<td>20</td>
</tr>
</tbody>
</table>

**Notes:**
* All parameters measured at Tc=25°C, nominal input voltage and full rated load unless otherwise noted. Refer to the CALEX Application Notes for definition of terms, measurement circuits and other information.
(1) Noise is measured per CALEX application notes. Measurement bandwidth is 0-20 MHz. RMS noise is measured over a 0.01-1 MHz bandwidth. To simulate standard PCB decoupling practices, output noise is measured with a 10µF tantalum and 0.01µF ceramic capacitor located 1 inch away from the converter.
(2) See our application note for picking the correct fuse size.
(3) The converter may be safely operated at any load from zero to the full rating. Dynamic response of the converter may degrade if the converter is operated with less than 25% output load.
(4) Load regulation is defined for loading/unloading both outputs simultaneously. Load range is 25 to 100%.
(5) Cross regulation is defined for loading/unloading one output while the other output is kept at full load. Load range is 25 to 100%.
(6) Short term stability is specified after a 30 minute warmup at full load, constant line and recording the drift over a 24 hour period.
(7) Case is tied to the CMN output pin.
(8) The functional temperature range is intended to give an additional data point for use in evaluating this power supply. At the low functional temperature the power supply will function with no side effects, however sustained operation at the high functional temperature may reduce the expected operational life. The data sheet specifications are not guaranteed over the functional temperature range.
(9) The case thermal impedance is specified as the case temperature rise over ambient per package watt dissipated.
(10) See the applications section for more information on applying the ON/OFF pin.
(11) Specifications subject to change without notice.
(12) Water Washability - Calex DC/DC converters are designed to withstand most solder/wash processes. Careful attention should be used when assessing the applicability in your specific manufacturing process. Converters are not hermetically sealed.

### Output Parameters*

<table>
<thead>
<tr>
<th>Model</th>
<th>24D5.850WD</th>
<th>24D12.400WD</th>
<th>24D15.320WD</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output Voltage</td>
<td>±5</td>
<td>±12</td>
<td>±15</td>
<td>VDC</td>
</tr>
<tr>
<td>Output Voltage Accuracy</td>
<td>MIN</td>
<td>±4.950</td>
<td>±11.90</td>
<td>±14.900</td>
</tr>
<tr>
<td></td>
<td>TYP</td>
<td>±5.000</td>
<td>±12.00</td>
<td>±15.000</td>
</tr>
<tr>
<td></td>
<td>MAX</td>
<td>±5.050</td>
<td>±12.100</td>
<td>±15.100</td>
</tr>
<tr>
<td>Rated Load Range (3)</td>
<td>TYP</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>MAX</td>
<td>±850</td>
<td>±400</td>
<td>±320</td>
</tr>
<tr>
<td>Load Regulation (4)</td>
<td>25% Max load to Max Load</td>
<td>TYP</td>
<td>0.1</td>
<td>0.1</td>
</tr>
<tr>
<td></td>
<td>MAX</td>
<td>0.5</td>
<td>0.5</td>
<td>0.5</td>
</tr>
<tr>
<td>Cross Regulation (5)</td>
<td>TYP</td>
<td>2.5</td>
<td>1.2</td>
<td>1.0</td>
</tr>
<tr>
<td>Line Regulation Vin = 9 - 36 VDC</td>
<td>TYP</td>
<td>0.1</td>
<td>0.1</td>
<td>0.1</td>
</tr>
<tr>
<td></td>
<td>MAX</td>
<td>0.7</td>
<td>0.3</td>
<td>0.3</td>
</tr>
<tr>
<td>Short Term Stability (6)</td>
<td>TYP</td>
<td>&lt;0.01</td>
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<td>% / 24 Hrs</td>
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<tr>
<td>Long Term Stability</td>
<td>TYP</td>
<td>&lt;0.1</td>
<td></td>
<td>% / kHrs</td>
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<tr>
<td>Noise, Peak - Peak (1)</td>
<td>TYP</td>
<td>100</td>
<td>120</td>
<td>150</td>
</tr>
<tr>
<td>RMS Noise</td>
<td>TYP</td>
<td>30</td>
<td>20</td>
<td>15</td>
</tr>
<tr>
<td>Temperature Coefficient</td>
<td>TYP</td>
<td>50</td>
<td>250</td>
<td>ppm / °C</td>
</tr>
<tr>
<td></td>
<td>MAX</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Notes:**
(7) Case is tied to the CMN output pin.
(8) The functional temperature range is intended to give an additional data point for use in evaluating this power supply. At the low functional temperature the power supply will function with no side effects, however sustained operation at the high functional temperature may reduce the expected operational life. The data sheet specifications are not guaranteed over the functional temperature range.
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“Not for New Design”

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General Specifications

<table>
<thead>
<tr>
<th></th>
<th>All Models</th>
<th>Units</th>
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<tr>
<td>ON/OFF Function</td>
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<tr>
<td>OFF Logic Level or Tie Pin to -Input (16)</td>
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<td>&lt;0.4</td>
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<tr>
<td>Open Circuit Voltage</td>
<td>TYP</td>
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<td>Input Resistance</td>
<td>TYP</td>
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<td>Converter Idle Current ON/OFF Pin Low</td>
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<td>6</td>
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<tr>
<td>Isolation (7)</td>
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<td>Isolation Voltage</td>
<td>MIN</td>
<td>700</td>
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<td>Input to Output</td>
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<td>350</td>
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<td>10µA Leakage</td>
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<td>Input to Output</td>
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<td>350</td>
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<td>Capacitance</td>
<td>TYP</td>
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<td>Environmental</td>
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<td>Case Operating Range</td>
<td>MIN</td>
<td>-40</td>
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<td>MAX</td>
<td>85</td>
<td>°C</td>
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<td>Case Functional Range (8)</td>
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<td>-50</td>
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<tr>
<td>MAX</td>
<td>100</td>
<td>°C</td>
</tr>
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<td>Storage Range</td>
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<td>-55</td>
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<tr>
<td>MAX</td>
<td>105</td>
<td>°C</td>
</tr>
<tr>
<td>Thermal Impedance (9)</td>
<td>TYP</td>
<td>16</td>
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<tr>
<td>Unit Weight</td>
<td>TYP</td>
<td>1.2</td>
</tr>
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<td>Mounting Options</td>
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</tr>
<tr>
<td>PCB Mounting Kits</td>
<td>MS6, MS8, MS15</td>
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<tr>
<td>Optional ON/OFF</td>
<td></td>
<td></td>
</tr>
<tr>
<td>-O Suffix on part Number</td>
<td>ON/OFF</td>
<td></td>
</tr>
</tbody>
</table>

Applications Information

You truly get what you pay for in a CALEX converter, a complete system oriented and specified DC/DC converter - no surprises, no external noise circuits needed, no heatsinking problems, just “plug and play”.

The WD Dual series like all CALEX converters carries the full 5 year CALEX no hassle warranty. We can offer a 5 year warranty where others can’t because with CALEX it’s rarely needed.

Keep reading, you’ll find out why.

General Information

The universal 9 to 36 volt input allows you to specify your system for operation from any 12, 24 or 28 volt nominal input.

Five sided shielding is standard along with specified operation over the full industrial temperature range of -40 to +85°C case temperature.

Applying the Input

Figure 1 shows the recommended input connections for the WD Dual DC/DC converter. A fuse is recommended to protect the input circuit and should not be omitted. The fuse serves to prevent unlimited current from flowing in the case of a catastrophic system failure.

When using the WD Dual be sure that the impedance at the input to the converter is less than about 0.15 ohms from DC to 100 kHz, this is usually not a problem in battery powered systems when the converter is connected directly to the battery. If the converter is located more than about 1-2 inches from the input source an added capacitor may be required. This capacitor should be connected directly at the input pins for proper operation.

The maximum permissible source impedance is a function of output power and line voltage. The impedance can be higher when operating at less than full power. The minimum impedance is required when operating at less that full power. The minimum impedance is required when operating with a 9 volt input at full load. The impedance reduces as the input voltage is raised or the power is reduced. In general you

![Diagram of WD Dual DC/DC Converter](image-url)

Pin location shown is for the mating PCB

![Pin Location Diagram](image-url)

Mechanical tolerances unless otherwise noted:

- X.XX ±0.03
- X.XXX ±0.005

Pin location shown is for the mating PCB

![Pin Location Diagram](image-url)

Figure 1.

If the source impedance driving the WD Converter is more than about 0.15 ohms at 100 kHz the optional capacitor C1 may be required (See text for more information). Optional transient protector diodes may be used if desired for added input and output protection. The fuse serves as a catastrophic failure protector and should not be omitted.
Picking An External Input Capacitor

If an input capacitor is needed at the input to the converter it must be sized correctly for proper converter operation. The curve “RMS Input Current vs Line Input” shows the RMS ripple current that the input capacitor must withstand with varying loading conditions and input voltages.

Several system tradeoffs must be made for each particular system application to correctly size the input capacitor.

The probable result of undersizing the capacitor is increased self heating, shortening its life. Oversizing the capacitor can have a negative effect on your products cost and size, although this kind of overdesign does not result in shorter life of any components.

There is no optimum value for the input capacitor. The size and capacity depend on the following factors:

1) Expected ambient temperature and your temperature derating guidelines.
2) Your ripple current derating guidelines.
3) The maximum anticipated load on the converter.
4) The input operating voltage, both nominal and excursions.
5) The statistical probability that your system will spend a significant time at any worst case extreme.

Factors 1 and 2 depend on your system design guidelines. These can range from 50 to 100% of the manufacturers listed maximum rating, although the usual derating factor applied is about 70%. 70% derating means if the manufacturer rated the capacitor at 1A RMS you would not use if over 0.7 A RMS in your circuit.

Factors 3 and 4 realistically determine the worst case ripple current rating required for the capacitor along with the RMS ripple current curve.

Factor 5 is not easy to quantify. At CALEX we can make no assumptions about a customers system so we leave to you the decision of how you define how big is big enough.

Suitable capacitors for use at the input of the converter are given at the end of this section.

Very Low Noise Input Circuit

Figure 2 shows a very low noise input circuit that may be used with the converters. This circuit will reduce the input reflected ripple current to less than 100mA peak-peak (Vin=24V, 20MHz bw). See the discussion above for the optimum selection of C1.

Remote ON/OFF (Optional)

The WD dual series has an optional Remote ON/OFF pin. This option allows a control signal to place the WD Dual in a very low power state (<6 mA TYP).

Do not drive this input from a logic gate directly. The ON/OFF pin must be left floating to turn the converter ON and allow normal operation. This input is noise sensitive and should not be routed all over your PCB.

When the ON/OFF pin is pulled low with respect to the -Input pin the converter is placed in a low power drain state. The ON/OFF pin turns the converter off while keeping the input bulk capacitors fully charged. Using the ON/OFF pin to control the WD’s operation prevents the large inrush current that would be experienced if the +Input pin was opened and closed.

The ON/OFF pin should never be pulled more than -0.3 volts below the -Input pin or have a voltage of greater than +3 volts applied to it.

Suggested Capacitor Sources

These capacitors may be used to lower your sources input impedance at the input of the converter. These capacitors will work for 100% load, worst case input voltage and ambient temperature extremes. They however, may be oversized for your exact usage, see “Picking an External Input Capacitor.” above for more information. You may also use several smaller capacitors in parallel to achieve the same ripple current rating. This may save space in some systems.

United Chemi-Con
Suggested Part: LXF50VB120M8X20LL
20 µF, 50V, 05ºC RATED
ESR=0.4 OHMS
Allowable ripple at 85ºC = .0 A

Nichicon
Suggested Part: UPR1H470MPH
470 µF, 50V, 05ºC RATED
ESR=0.0 OHMS
Allowable ripple at 85ºC = .0 A

Panasonic
Suggested Part: ECAHFQ5
50 µF, 50V, 05ºC
ESR=0.6 OHMS
Allowable ripple at 85ºC = .0 A

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Applying the Output

Figure 1 shows typical output connections for the WD Dual. In most applications no external output capacitance will be necessary. Only your normal 1 to 10 μF tantalum and 0.001 to 0.1 μF ceramic bypass capacitors sprinkled around your circuit as needed locally are required. Do not add extra output capacitance and cost to your circuit “Just Because”.

If you feel you must add external output capacitance, do not use the lowest ESR, biggest value capacitor that you can find! This can only lead to reduced system performance or oscillation. See our application note “Understanding Output Impedance for Optimum Decoupling” for more information, and by all means use our factory tested low noise circuit provided.

Ultra Low Noise Output Circuit

The circuit shown in figure 3 can be used to reduce the output noise to below 10 mV p-p over a 20 MHz bandwidth. Size inductor L1 appropriately for the maximum expected load current. All of the ground connections must be as short as possible back to the CMN pin. The filter should be placed as close to the WD Dual as possible, even if your load is at some distance from the converter.

![Ultra Low Noise Output Circuit Diagram]

Figure 3.
This circuit can reduce the output noise to below 10 mV p-p over a 20 MHz bandwidth. Size inductor L1 appropriately for the maximum expected load current. The filter should be constructed as close as possible to the converter and all of the ground connections must be as short as possible back to the CMN pin.

Single Ended Loads

The WD Dual may be operated as a single ended output to get either 10, 24 or 30 volts output as shown in figure 1. If the output is operated single ended then the CMN pin should be left floating.

Full power is available when operating single ended. For example: If a 24D5.850WD is operated as a 0 volt output then a full 0.850 amps is available at 0 volts.

Operation With Very Light Loads

Dynamic response and cross regulation of the WD Dual will degrade when the unit is operated with less than about 25% of full rated power. If this is a problem the most lightly loaded output may be “Pre-Loaded” with a resistor to common as needed. The exact amount of preloading required is dependent on your system requirements, so some experimentation is necessary to arrive at the optimum value.

Grounding

The input and output sections are fully floating from each other. They may be operated fully floating or with a common ground. If the input and output sections are connected either directly at the converter or at some remote location from the converter it is suggested that a 1 to 10 μF, 0.5 to 5 Ohm ESR capacitor bypass be used directly at the converters output pins. These capacitors prevent any common mode switching currents from showing up at the converters output as normal mode output noise. See “Applying the Output” for more information on selecting output capacitors.

Also see the CALEX application note “Dealing With Common Mode Noise” for more information on using common grounds.

Case Grounding

The copper case serves not only as a heat sink but also as an EMI shield. The 0.017 inch thick case provides >20 dB of absorption loss to both electric and magnetic fields at 100 kHz, while at the same time providing 20 to 40% better heat sinking over competitive thin steel, aluminum or plastic designs.

The case shield is tied to the CMN output pin. This connection is shown on the block diagram. The case is floating from the input sections. The input is coupled to the outputs only by the low 350 pF of isolation capacitance. This low I/O capacitance insures that any AC common mode noise on the inputs is not coupled to your output circuits.

Compare this isolation to the more usual 1000 - 2000 pF found on competitive designs and you will see that CALEX provides the very best DC and AC isolation available. After all, you are buying an isolated DC/DC to cut round loops. Don’t let the isolation capacitance add them back in.
“Not for New Design”

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Temperature Derating

The WD Dual series can operate up to 85°C case temperature without derating. Case temperature may be roughly calculated from ambient by knowing that the case temperature rise is approximately 16°C per package watt dissipated.

For example: If a WD Dual converter is delivering 8 Watts with a 24 volt input, at what ambient could it expect to run with no moving air and no extra heatsinking?

Efficiency of the converter is approximately 76% at 8 watts of output power, this leads to an input power of about 10.5 Watts. The case temperature rise would be 10.5 - 8 Watts or 2.5 Watts x 16 = 40°C. This number is subtracted from the maximum case temperature of 85°C to get: 45°C.

This example calculation is for the WD Dual without any extra heat sinking or appreciable air flow. Both of these factors can greatly effect the maximum ambient temperature (see below). Exact efficiency depends on input line and load conditions, check the efficiency curves for exact information.

This is a rough approximation to the maximum ambient temperature. Because of the difficulty of defining ambient temperature and the possibility that the loads dissipation may actually increase the local ambient temperature significantly, these calculations should be verified by actual measurement before committing to a production design.

Remember, it is the system designer’s responsibility to be sure that the case temperature of the WD Dual does not exceed 85°C for maximum reliability in operation.

Typical Performance (Tc=25°C, Vin - Nom VDC, Rated Load)