## Features

- Very Low Noise, < 50 mV P-P Maximum
- PCB Mounting with Optional Heat Sink or Chassis Mount Versions
- Efficiencies to 87\%
- Common and Differential Mode Input Filtering
- Remote Sense On +5 Volt Output
- Single and Dual Outputs are Isolated From Each Other
- No derating to $80^{\circ} \mathrm{C}$ Case Temperature
- Five Year Warranty

| Selection Chart |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Model | Input Range VDC |  | Outputs VDC | Outputs mA |
|  | Min | Max |  |  |
| 12T5.12K | 9.0 | 18.0 | 5, $\pm 12$ | 5000, $\pm 1250$ |
| 12T5.15K | 9.0 | 18.0 | 5, $\pm 15$ | 5000, $\pm 1000$ |
| 24T5.12K | 18.0 | 36.0 | 5, $\pm 12$ | 5000, $\pm 1250$ |
| 24T5.15K | 18.0 | 36.0 | 5, $\pm 15$ | 5000, $\pm 1000$ |
| 48T5.12K | 36.0 | 72.0 | 5, $\pm 12$ | 5000, $\pm 1250$ |
| 48T5.15K | 36.0 | 72.0 | $5, \pm 15$ | 5000, $\pm 1000$ |

## Description

The 55 Watt Triple Series consists of separate power sections for the single and dual outputs. These power sections are operated in anti-phase to each other to reduce the ripple current stress on the input components and the reflected input ripple. The main benefit of two separate power sections is the excellent regulation achieved by all outputs. An order of magnitude regulation improvement is obtained over competitive designs. There is no cross regulation between +5 output and the dual outputs. This means that the dual output voltages are independent of the +5 Volt loading and visa versa.

Excellent noise performance is attained by using a 0.040 inch thick aluminum case, pot core and toroidal magnetics, double shielded transformers and both normal mode and common mode input filtering.

The input and outputs are protected from overvoltage by transient voltage suppressor diodes. The outputs are protected from faults with pulse by pulse (igitel current limiting.


| Input Parameters* |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Model |  | 12T5.12K | 12T5.15K | 24T5.12K | 24T5.15K | 38T5.12K | 48T5.15K | Units |
| Voltage Range | $\begin{aligned} & \hline \text { MIN } \\ & \text { MAX } \end{aligned}$ | $\begin{gathered} 1 \\ 9.0 \\ 18.0 \end{gathered}$ |  | $\begin{aligned} & 18.0 \\ & 36.0 \end{aligned}$ |  | $\begin{aligned} & 36.0 \\ & 72.0 \end{aligned}$ |  | VDC |
| Reflected Ripple (2), 0-20MHz bw | $\begin{aligned} & \hline \text { TYP } \\ & \text { MAX } \end{aligned}$ | $\begin{aligned} & 110 \\ & 250 \end{aligned}$ |  | $\begin{gathered} 60 \\ 150 \\ \hline \end{gathered}$ |  | $\begin{aligned} & 35 \\ & 80 \end{aligned}$ |  | mA P-P |
| Input Current Full Load No Load | $\begin{aligned} & \hline \text { TYP } \\ & \text { TYP } \end{aligned}$ | $\begin{gathered} 5630 \\ 50 \end{gathered}$ |  | $\begin{gathered} 2715 \\ 35 \end{gathered}$ |  | $\begin{gathered} 1325 \\ 30 \end{gathered}$ |  | mA |
| Efficiency | TYP | 81 |  | 85 |  | 87 |  | \% |
| Switching Frequency | TYP | 80 |  |  |  |  |  | kHz |
| Maximum Input Overvoltage, 100ms No Damage | MAX | 23 |  | 45 |  | 85 |  | VDC |
| Turn-on Time, 1\% Output Error | TYP | 70 |  |  |  |  |  | ms |
| Recommended Fuse |  | (3) |  |  |  |  |  | AMPS |


| Output Parameters* |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Model |  | 12 T 5.12 K 12 T 5.15 K <br> 24 T .12 K 24 T 5.15 K <br> 48 T .12 K 48 T 5.15 K | $\underset{48 \mathrm{~T} 5.12 \mathrm{~K}}{2 \mathrm{~K} .12 \mathrm{~K}}$ | 12T5.15K 24T5.15K 48T5.15K | Units |
| Output Voltage |  | +5 | $\pm 12$ | $\pm 5$ | VDC |
| Rated Current (4) | $\begin{aligned} & \mathrm{MIN} \\ & \mathrm{MAX} \end{aligned}$ | $\begin{gathered} 50 \\ 5000 \end{gathered}$ | $\begin{gathered} 310 \\ 1250 \end{gathered}$ | $\begin{array}{r} 50 \\ 1000 \end{array}$ | mA |
| Voltage Range $100 \%$ Load (5) | $\begin{aligned} & \text { MIN } \\ & \text { TYP } \\ & \text { MAX } \end{aligned}$ | $\begin{aligned} & 4.950 \\ & 5.000 \\ & 5.050 \end{aligned}$ | $\qquad$ <br> 11.925 1207 | $\begin{aligned} & 14.925 \\ & 15.000 \\ & 15.075 \end{aligned}$ | VDC |
| Output Balance (Plus to Minus Output, Full Load) | $\begin{aligned} & \text { TYP } \\ & \text { MAX } \end{aligned}$ | N/A |  | $\begin{aligned} & 0.5 \\ & 0.7 \end{aligned}$ | \% |
| $\begin{array}{rc}\text { Load Regulation } & 25 \%-100 \% \\ 1-100 \%\end{array}$ | $\begin{array}{\|l\|} \hline \text { TYP } \\ \text { MAX } \\ \text { TYP } \\ \hline \end{array}$ | $\begin{gathered} \hline 0.02 \\ 0.2 \\ 0.02 \\ 0.2 \\ \hline \end{gathered}$ | $\square \begin{array}{r} 5.8 \\ 1.2 \\ 2.0 \\ 4.0 \\ \hline \end{array}$ | $\begin{aligned} & 0.7 \\ & 1.2 \\ & 2.0 \\ & 4.0 \\ & \hline \end{aligned}$ | \% |
| Cross Regulation (6) | $\begin{array}{\|l\|} \hline \text { TYP } \\ \text { MAX } \\ \hline \end{array}$ | NA 1.2 1.2 |  |  | \% |
| Line Regulation Vin = Min-Max VDC | $\begin{aligned} & \text { TYP } \\ & \text { MAX } \end{aligned}$ | $\begin{gathered} 0.02 \\ 0.2 \\ \hline \end{gathered}$ |  |  | \% |
| Short Term Stability (7) | TYP | 0.02 |  |  | \% |
| Long Term Stability |  | 0.2 |  |  | \%/kHrs |
| Transient Response (8) |  | 500 | 400 | 400 | $\mu \mathrm{s}$ |
| Dynamic Response (9) | T/P | 250 | 250 | 200 | mV peak |
| Input Ripple Rejection (10), | TYP | 25 | 15 | 15 | dB |
| Noise, $0-20 \mathrm{MHz}$ bw | $\begin{array}{\|l\|} \hline \text { TYP } \\ \text { MAX } \\ \hline \end{array}$ |  | $\begin{aligned} & 15 \\ & 50 \end{aligned}$ |  | mV P-P |
| Temperature Coefficier | $\begin{array}{\|l\|l\|} \hline \text { TYP } \\ \text { MAX } \end{array}$ | $\begin{gathered} 50 \\ 100 \\ \hline \end{gathered}$ | $\begin{aligned} & 50 \\ & 200 \\ & \hline \end{aligned}$ | $\begin{aligned} & 50 \\ & 200 \\ & \hline \end{aligned}$ | ppm/ ${ }^{\circ} \mathrm{C}$ |
| Overvoltage Clamp (11) | TYP | 6.8 | 15 | 18 | VDC |
| Short Circuit Protection to Common for all Outputs |  | Provides minim | of 8 hours continuous pro thermal overload tec | on with current limiting ues |  |

## NOTES

* All parameters measured at $\mathrm{Tc}=25^{\circ} \mathrm{C}$, nominal input voltage and full rated load unless otherwise noted. Refer to the CALEX Application Notes for the definition of terms, measurement circuits and other information.
(2) Noise is measured per CALEX Application Notes. Measurement bandwidth is $0-20 \mathrm{MHz}$.
(3) Determine the correct fuse size by calculating the maximum DC current drain at low line input, maximum load and then adding 20 to $25 \%$ to get the desired fuse size. A slow blow type fuse is recommended. For reverse voltage protection on the input this fuse must be used.
(4) Minimum load is required for rated regulation only, no module damage will occur if the output is run at less than minimum load. Maximum output power on the dual section is 30 Watts (i.e. one output can draw 30 Watts and the other 0 Watts). Regulation degrades with substantial loading unbalance.
(5) The remote sense pins must be connected to their respective outputpins for proper output voltage and regulation. The combined drop on each output line to it's respective remote sense pin must be less than 0.3 volts or 0.6 volts for both sense lines combined.
(6) Cross regulation is defined as the change in one output when the other output is changed from minimum to maximum load.
(7) Short term stability is specified after a 30 minute warm-up at full load, constant line, load and ambient conditions.
(8) Transient response is defined as the time for the output to settle from a 50 to $75 \%$ step load change to a 2\% error band (rise time of step $=2 \mu \mathrm{Sec}$ ).
(9) Dynamic response is defined as the peak overshoot during a transient as defined in note 8 above.
(10) The input ripple rejection is specified for DC to 120 Hz ripple with a modulation amplitude of $1 \%$ of Vin.

| General Specifications* |  |  |  |
| :---: | :---: | :---: | :---: |
| All Models |  |  | Units |
| ON/OFF Function (12) |  |  |  |
| ON Logic Level or Leave Pin Open | MIN | 4.5 | VDC |
| OFF Logic Level | MAX | 1.2 | VDC |
| Input Resistance | TYP | 2 | k ohms |
| Converter Idle Current, ON/OFF Pin Low | TYP | 25 | mA |
| Isolation |  |  |  |
| Isolation Voltage Input to Either Output Single to Dual Output Input-Case Either Output to Case $10 \mu \mathrm{~A}$ Leakage | MIN <br> MIN <br> MIN <br> MIN | $\begin{aligned} & 1544 \\ & 700 \\ & 250 \\ & 1544 \end{aligned}$ | VDC |
| Input to Output Capacitance Input to Single Output Input to Dual Output Single to Dual Output | $\begin{aligned} & \text { TYP } \\ & \text { TYP } \\ & \text { TYP } \end{aligned}$ | $\begin{aligned} & 100 \\ & 150 \\ & 75 \end{aligned}$ | pF |
| Environmental |  |  |  |
| Case Operating Range No Derating | $\begin{array}{\|l} \hline \mathrm{MIN} \\ \mathrm{MAX} \\ \hline \end{array}$ | $\begin{aligned} & \hline-25 \\ & 80 \\ & \hline \end{aligned}$ | ${ }^{\circ} \mathrm{C}$ |
| Case Funtional Range (13) | $\begin{aligned} & \mathrm{MIN} \\ & \mathrm{MAX} \end{aligned}$ | $\begin{aligned} & -40 \\ & 90 \\ & \hline \end{aligned}$ | ${ }^{\circ} \mathrm{C}$ |
| Storage Range | $\begin{array}{\|l} \hline \text { MIN } \\ \text { MAX } \end{array}$ | $\begin{aligned} & \hline-40 \\ & 100 \\ & \hline \end{aligned}$ | ${ }^{\circ} \mathrm{C}$ |
| Thermal Impedance (14) Pin Mount Version Option -HS (Heat Sink) Option -CM (Chassis Mount) | $\begin{aligned} & \text { TYP } \\ & \text { TYP } \end{aligned}$ | $\begin{gathered} 2.7 \\ 1.8 \\ (14) \end{gathered}$ | ${ }^{\circ} \mathrm{C} /$ Watt |
| Thermal Shutdown Case Temperature | TYP | 90 | ${ }^{\circ} \mathrm{C}$ |
| General |  |  |  |
| Unit Weight 14 |  |  |  |
| Mounting Kits |  | See C |  |

(11) For module protection only, see al on te 3.
(12) The ON/OFF pin is Open C llegorTTL, CMOS, and relay compatible. The inputto gicemino referenced to -Input (pin 2).
(13) The functionalt then tu enge is intended to give an additional data point for $u$ e in $\epsilon$ /aruating this power supply. At the low functional temper the the power supply will function with no side effects, however, sustained operation at the high functional temperature will reduce expected operational life. The data sheet specifications are not guaranteed over the functional temperature range.
(14) The case thermal impedance is specified as the case temperature rise over ambient per package watt dissipated. The thermal resistance of the Chassis Mount version depends on the mounting surface. If the mounting surface is a poor thermal conductor the thermal resistance can be as high as $2.7^{\circ} \mathrm{C} /$ watt. If the mounting surface is an excellent thermal conductor the thermal resistance can be below $1^{\circ} \mathrm{C} /$ watt.
(15) Water Washability - Calex DC/DC converters are designed to withstand most solder/wash processes. Careful attention should be used when assessing the applicability in your specific manufacturing process. Converters are not hermetically sealed.

## Mounting Configuration Options

To order the optional heat sink on the pin mount version place a "-HS" suffix on the part number. To order the chassis mount version place a "-CM" suffix on the part number. The heat sink and chassis mount options cannot be used together.

Heat Sink Option (-HS suffix)


Mechanical tolerances unless otherwise noted:
X.XX dimensions: $\pm 0.020$ inches
X.XXX dimensions: $\pm 0.005$ inches

Seal around terminals is not hermetic. Do not immerse units in any liquid.

| Pin | Function | Pin | Function |
| :---: | :---: | :---: | :---: |
| 1 | NO PIN | 8 | +5 V CMN SENSE |
| 2 | -INPUT | 9 | +5 V CMN |
| 3 | -INPUT | 10 | +5 OUTPUT |
| 4 | +INPUT | 11 | $+5 V$ SENSE |
| 5 | +INPUT | 12 | -DUAL OUTPUT |
| 6 | ON/OFF | 13 | DUAL CMN |
| 7 | CASE | 14 | + DUAL OUTPUT |

# 55 Watt K Triple Series DC/DC Converters 

## Application Guidelines Inputs

The input should be fused as per note number 3 . The case is connected to the -INPUT pins through 1.5 Megohms in parallel with $0.01 \mu \mathrm{~F}$. The case may be left floating in most applications. The noise performance of the converter may improve or degrade with the case connected to other inputs or outputs depending on your system grounding.

The remote ON/OFF pin may be left floating if it is not used. See "Understanding the Remote ON/OFF Function" application note for more information on this feature.

The dual input pins ( 2,3 and 4,5 ) should be paralleled to share the input current. Figure 1 shows the recommended input connections.

Figure 1.


Parallel both + and -INPUT pins. Fusing the input is recommended.

## Single Output

The single output is independent of the loading on the dy al output section. The single output also features provision tr remote sense connections. These allow the power u oly to correct for line drops of up to 0.3 volts per legor ?. vons total. The remote sense connections should be made vith twisted pair wire or closely coupled PCB traces Ti ere sapproximately 2 mA of current flowing in the remote sen e elines. If the remote sense is not to be used, these pi is muve be connected to their respective output pins for pion put voltage accuracy and regulation. See " A ppr, in" the Remote Sense and Trim Functions On DC/LC CP nverters" application note for more information. The out, at common on the single section is electrically isolated from the dual section to aid in proper system grounding. Figure 2 shows the single output connections with remote sense.

Figure 2.


The remote sense pins must be connected to their respective outputs. If the remote sense feature is not used, connect the sense pins to their outputs directly at the converter.

## Dual Output

The dual outputs are cross regulated to each other but independent of the loading on the single output. The dual output uses a cross regulation scheme where the plus and minus output voltage is regulated as an average voltage. This allows improved regulation on both outputs. If the outputs are loaded equally and the regulation is checked, then the apparent regulation is much closer to $0.5 \%$. This data shows the worst case result of changing each output independently. The output common on the dual section is electrically isolated from the +5 volt section to aid in proper system grounding. If 24 volts or 30 volts are required, then the appropriate output can be connected to the system ground and the output current can be taken from the other output. In this situation the dual section's common pin (Pin 13) should be left unconnected. Full output power ( 30 watts) is available in this configuration. Figure 3 shows the dual output connections.


The dual outputs can also be used with substantial unbalance and as single-ended outputs by leaving Pin 13 unconnected.

## Mounting Guidelines

The 55 Watt Triple Series can be supplied in either a pin mount version or a chassis mount (screw terminal) version. It is suggested that when using the pin mount version, the 4-40 hold down screws provided in the bottom of the case be used to secure the unit to the PCB. These screws should be tightened before soldering to avoid solder joint stress.

The chassis mount version can be mounted on its back with the four $4-40$ hold down screws provided. The mounting surface should be flat to within 0.01 inches to prevent warping the case. The mounting can then serve as additional heat sinking. For optimum heat sinking, silicone grease is recommended over the so called "dry pads." Additional heat sinking will lower internal temperatures and increase the expected life. When chassis mounting remember that the case is connected to the - INPUT pins through 1.5 Megohms in parallel with $0.01 \mu \mathrm{~F}$. The breakthrough voltage of this connection is greater than 250VDC.

The "-HS" heat sink option lowers the thermal resistance from 2.7 to $1.8^{\circ} \mathrm{C} /$ Watt dissipated. It also increases the heat removing efficiency of any cooling air flow. If the 55 Watt Triple is to be placed in a small enclosure where the power delivered to the load or the power dissipated in the 55 Watt Triple converter can raise the ambient temperature then care must be taken to insure that the case temperature does not exceed the $80^{\circ} \mathrm{C}$ limit for proper operation.

Typical Performance $\left(\mathrm{Tc}=25^{\circ} \mathrm{C}\right.$, Vin $=48 \mathrm{VDC}$, Rated Load).


Typical Performance ( $\mathrm{Tc}=25^{\circ} \mathrm{C}$, Vin $=48$ VDC, Rated Load).







